ASIF JAHANGIR CHOWDHURY

1029/C, Hill View Regency, Hill View Road, East Nasirabad, Chittagong, Bangladesh
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Research Interests

- Integrated circuit, Embedded system design
- Renewable and sustainable energy
- Nanotechnology, Nano-photonics
- Solid State Device Modeling

Education

Bachelor of Science April, 2012
Electrical and Electronic Engineering
Bangladesh University of Engineering & Technology (BUET)
CGPA : 3.89/4.00
Concentration: Electronics, Minor: Communication

Higher Secondary Certificate Examination September, 2006
Chittagong College, GPA: 5.00 /5.00

Research Experience

Thesis: An Efficient Approach to Low-Leakage Power VLSI Design using Variable Body Biasing.
Overview: In this thesis work a new method has been proposed to reduce static power in the CMOS VLSI circuit using Variable Body Biasing technique without being penalized in area requirement and circuit performance.

Publication

- **Chowdhury, Asif Jahangir**; Rizwan, Md.Shahriar; Nibir, Shahriar Jalal; Siddique, Md.Rifat Alam, "A new leakage reduction method for ultra low power VLSI design for portable devices," Power, Control and Embedded Systems (ICPCES), 2012 2nd International Conference on , pp.1,4, 17-19 Dec. 2012
- **Chowdhury A. J.**, Rizwan M. S., Islam M. S. "An Efficient Approach to Low-Leakage Power VLSI Design using Variable Body Biasing." International Conference on Electrical, Computer, Electronics and Communication Engineering. Venice, Italy, April 11-13, 2012, issue 64, page no. 263-267
- Nibir, Shahriar Jalal; Siddique, Md.Rifat Alam; **Chowdhury, Asif Jahangir**; Rizwan, Md.Shahriar, "A comparative study of rooftop micro wind turbines on Bangladesh perspective," Power, Control and Embedded Systems (ICPCES), 2012 2nd International Conference on , pp.1,5, 17-19 Dec. 2012

Work Experience

Software Engineer July 2012- present
Solution Lab,
Samsung Research Institute Bangladesh (SRBD), Dhaka, Bangladesh

- Worked in Tizen group - developed tools for testing TIZEN API
- Icon of the month [February 2013]

Skills

Programming Languages: MATLAB, C/C++, Java, Javascript, Shell Script, HTML, Assembly,Verilog
Simulation and Design Tools: HSPICE, Comsol, Cadence Tools, AutoCAD, MATLAB, Proteus (ISIS, ARES)
Typesetting Software: LaTeX
IDE: Eclipse(Android, Tizen), AVR studio, CodeVision
Other Skills: microcontroller programming, PCB design

Undergraduate Courses

Analog Integrated Circuits	VLSI-I/II
Hetero-junction And Semiconductor Devices	Semiconductor Device Theory
Digital Signal Processing	Control System
Microwave theory	Digital System Design
Microprocessor and Interfacing	

Projects

- Automatic guitar tuner.
- Temperature and moisture measurement of a room and remote sensing.
- Automatic railway gate control.
- Analog amplitude modulation and pc interfacing using MATLAB.
- Design of a four bit ALU with shifter.
- Delay and area optimization of a 3 input XOR by efficient system and layout design.
- Security System Design of a museum.
- Electrical wiring design of a 4 storied building.
- Buzzer system design.
- Design and making of a transformer.

Achievements

- SRBD ***Icon of the Month*** for February, 2013
- **Champion** in Regional Math Olypmpiad in 2003 and 2005
- University Talent Scholarship (2007-2008,2008-2009,2009-2010)
- Deans list in all four levels of Undergraduate Studies
- Board(Chittagong board) Scholarship in talent pool in Higher Secondary Certificate examination
- Board(Chittagong board) Scholarship in talent pool in Secondary School Certificate examination

References

Prof. Md. Shafiqul Islam, Dept. of Electrical & Electronic Engg., BUET, Bangladesh.
email: islams@eee.buet.ac.bd
Prof. Md. Ali Chowdhury, Dept. of Electrical & Electronic Engg., BUET, Bangladesh.
email: mac@eee.buet.ac.bd